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Sheet 1

	Complete If Known	
	Application Number	10/552,601
	Filing Date	October 5, 2005
	First Named Inventor	Terablaze et al.
	Art Unit	2616
	Examiner Name	Unassigned
	Attorney Docket Number Terablaze 4	

U. S. PATENT DOCUMENTS Examinar Document Number Publication Date Name of Patentee or Pages, Columns, Lines, Where MM-DD-YYYY Applicant of Cited Document Relevant Passages or Relevant Number-Kind Code^{2 (F tress)} /CH/ ^{US-} 5,905,725 05/18/1999 Sindbu et al. /CH/ ^{US-} 6,320,861 11/20/2001 Adam et al. CH/ US-6,532,503 03/11/2003 Lindeborg et al. /**CH**7 US- 2002/0169921 A1 11/14/2002 4 Saltoh US-US. US. US-US-US-US-US-US-118-US-

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite	Foreign Patent Document Country Code ³ Thirriber ⁴ Kind Code ⁴ (Il known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Olted Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	Τ ^d
/CH/	5	EP 0 612 171 A2	08/24/1994			
/CH/	6	WO 02/09364 A2	01/31/2002			
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/CH/	7	Naraght-Pour et al., "A Multiple Shared Memory Switch," System Theory IEEE Proc of the 28th Southeastern Symposium on Baton Rouge, IEEE Comput. Soc , pgs 50-54 (2/31/196).	
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